Approval

SAMSUNG
ELECTRONICS

Customer : GA   **DATE : 08. Apr. 2010**

SAMSUNG TFT-LCD

MODEL : LTA550HJ07

The Information Described in this Specification is Preliminary and can be changed without prior notice

NOTE :

Customer's Approval		APPROVED BY *Jeong min Heo*	DATE 08. Apr. 2010
SIGNATURE	DATE	PREPARED BY *hyoung-gu Yun*	DATE 08. Apr. 2010

LCD Business

Samsung Electronics Co . , LTD.

Contents

Revision History

Date	Rev. No	Page	Summary
Mar. 11. 2010	000	all	First issued
Apr. 08. 2010	001	Page 9 Page 11	-Add Power consumption of Control board - Add Power consumption of Backlight

General Description

Description

LTA550HJ07 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 55.0" is 1920 x 1080 and this model can display up to 1.07 Billion colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV

Features
- RoHS compliance (Pb-free)
- High contrast ratio & aperture ratio with wide color gamut
- SPVA(Super Patterned Vertical Align) mode
- Wide viewing angle ($\pm 178°$)
- High speed response (& Natural Motion (DFR: Double Frame Rate))
- FHD resolution (16:9)
- Low Power consumption
- Direct Type 16 CCFLs(Cold Cathode Fluorescent Lamp)
- DE(Data Enable) mode
- 4ch LVDS (Low Voltage Differential Signaling) interface (4pixel/clock)

General Information

Items	Specification	Unit	Note
Module Size	1,286 (H) X 745 (V)	mm	± 1.0mm
	62.5		
Weight	18,000 (Max)	g	
Pixel Pitch	0.630(H) x 0.630(W)	mm	
Active Display Area	1209.6(H) X 680.4(V)	mm	
Surface Treatment	Antiglare, Hard-coating(3H)		
Display Colors	1.07 Billion	colors	
Number of Pixels	1920 x 1080	pixel	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Luminance of White	450 (Typ.)	cd/m^2	

1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{DD}	GND-0.5	13.2	V	(1)
Dimming Control	Max. Lum	-	5	V	
Storage temperature	T_{STG}	-20	60	℃	(2)
Operating temperature	T_{OPR}	0	50	℃	
Surface temperature	T_{SUR}	0	60	℃	(3)
Shock (non - operating)	X,Y,Z		30	G	(4)
Vibration (non - operating)	V_{NOP}	-	1.5	G	(5)

Note (1) Ta= 25 ± 2 °C
(2) Temperature and relative humidity range are shown in the figure below.
   a. 90 % RH Max. (Ta ≤ 39 °C)
   b. Relative Humidity is 90% or less. (Ta > 39 °C)
   c. No condensation
(3) Although abnormal visual problems can be occurred in T_{SUR} range, the polarizer is not damaged in this range.
(4) 11ms, sine wave, one time for ±X, ±Y, ±Z axis
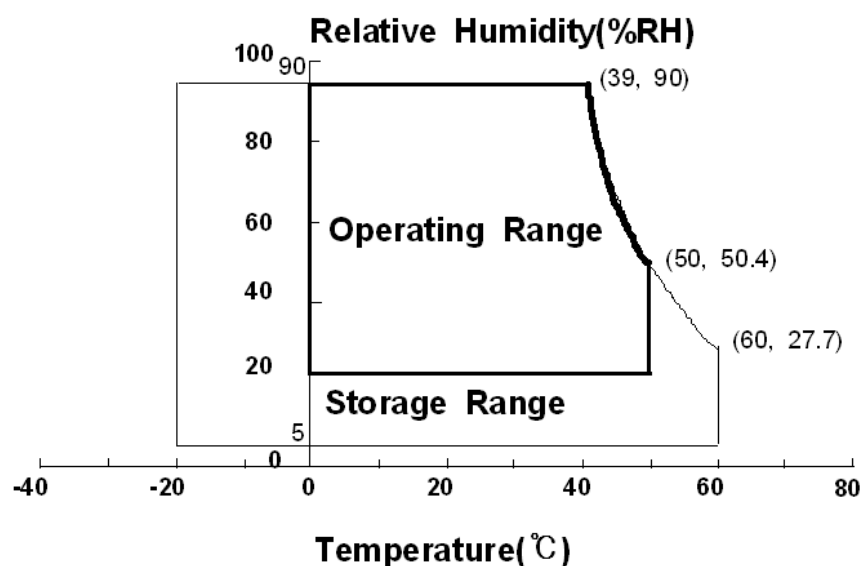(5) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

Fig. Temperature and Relative humidity range

2. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent.
Measuring equipment : TOPCON RD-80S, TOPCON SR-3, ELDIM EZ-Contrast

(Ta = 25 $\pm$ 2°C, VDD=12V, fv= 120Hz, f_{DCLK} = 297.0MHz, Lamp current = 11.5mA)

| Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R		3,500	5,000	-		(1) SR-3
Response Time	G-to-G	Tg		-	6	-	msec	(3) RD-80S
Luminance of White (Center of screen)		Y_L		400	450	-	cd/m²	(4) SR-3
Color Chromaticity (CIE 1931)	Red	Rx	Normal q**L,R** =0 q**U,D** =0 Viewing Angle	TYP. -0.03	0.637	TYP. +0.03		(5),(6) SR-3
		Ry			0.326			
	Green	Gx			0.287			
		Gy			0.607			
	Blue	Bx			0.150			
		By			0.055			
	White	Wx			0.280			
		Wy			0.290			
Color Gamut		-		-	72	-	%	(5) SR-3
Color Temperature		-		-	10,000	-	K	
Viewing Angle	Hor.	q_L	C/R≥10	75	89	-	Degree	(6) EZ-Contrast
		q_R		75	89	-		
	Ver.	q_U		75	89	-		
		q_D		75	89	-		
White Brightness Uniformity (9 Points)		B_{uni}		-	-	25	%	(2) SR-3

- Test Equipment Setup

   The measurement should be executed in a stable, windless and dark room between
   40min and 60min after lighting the back light at the given temperature for stabilization
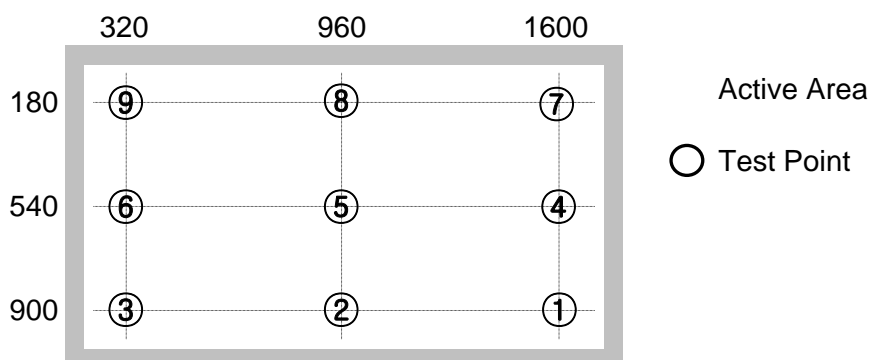   of the back light. This should be measured in the center of screen.

   Environment condition : Ta = 25 $\pm$ 2 °C

Photo detector	Field
SR-3	2˚
RD-80S	1˚

Photo detector

Field

SR-3 : 50㎝
RD-80S : 50㎝

TFT - LCD Module

LCD Panel

The center of the screen
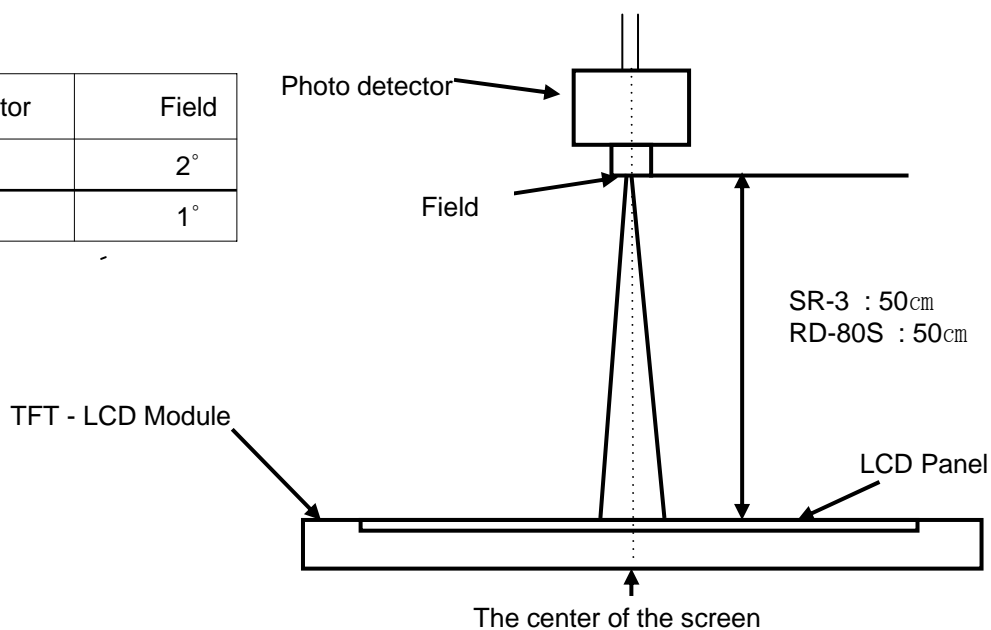
- Definition of test point

320   960   1600

180   ⑨   ⑧   ⑦   Active Area

540   ⑥   ⑤   ④   ◯ Test Point

900   ③   ②   ①

Note (1) Definition of Contrast Ratio (C/R)
: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G\max}{G\min}$$

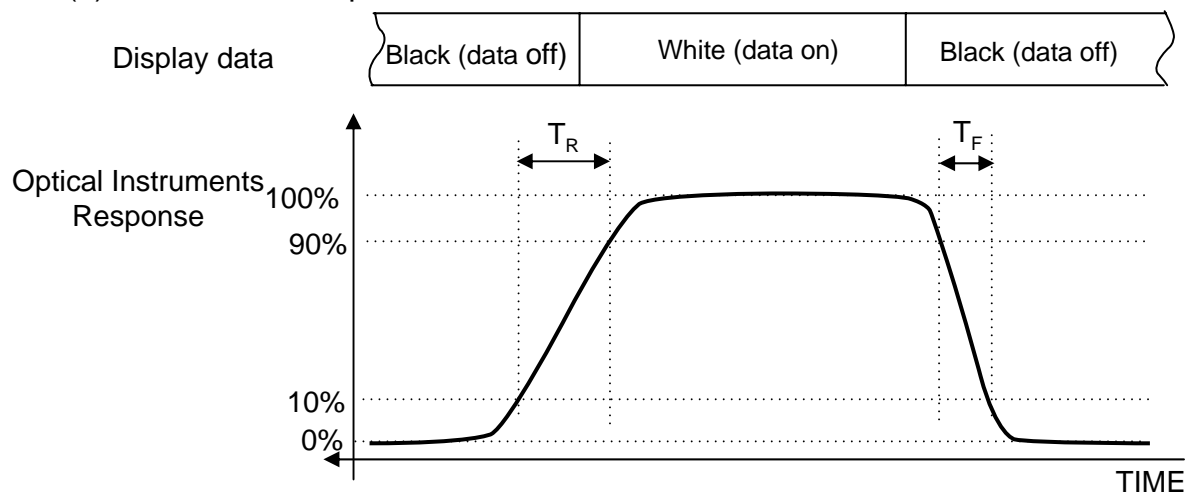Gmax : Luminance with all pixels white
Gmin : Luminance with all pixels black

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Note (2) Definition of 9 points brightness uniformity (Test pattern : Full White)

$$Buni = 100 * \frac{(B\max - B\min)}{B\max}$$

Bmax : Maximum brightness
Bmin : Minimum brightness

Note (3) Definition of Response time : Sum of Tr, Tf

Display data	Black (data off)	White (data on)	Black (data off)

Optical Instruments Response
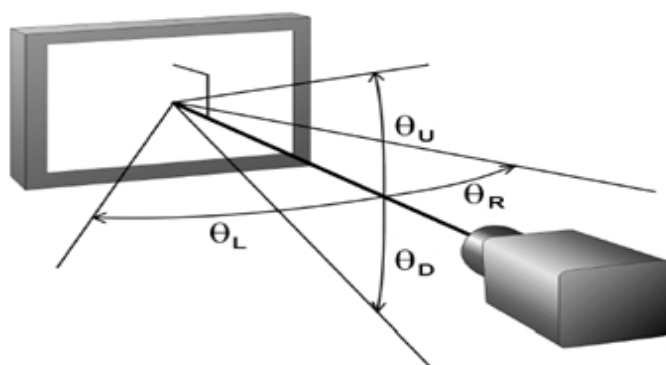
100%
90%

10%
0%

T_R T_F

TIME

Note (4) Definition of Luminance of White : Luminance of white at center point ⑤

Note (5) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point ⑤

Note (6) Definition of Viewing Angle
: Viewing angle range (C/R ≥10)

Θ_U
Θ_R
Θ_L
Θ_D

3. Electrical Characteristics

3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

Ta = 25°C $\pm$ 2 °C

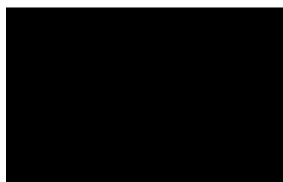| Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V_{DD}	10.8	12.0	13.2	V	(1)
Current of Power Supply	(a) Black	I_{DD}	-	700	1000	mA	(2),(3)
	(b) White		-	700	1000	mA	
	(c) H-STRIPE		-	1200	1500	mA	
Power Consumption (Control)		Pc	-	14.4	18	Watt	
Vsync Frequency		f_V	90	120.0	125	Hz	
Hsync Frequency		f_H	100	135.0	140	kHz	
Main Frequency		f_{DCLK}	240	297.0	310	MHz	
Rush Current		I_{RUSH}	-	-	7	A	(4)

Note (1) The ripple voltage should be controlled under 10% of V_{DD}.
(2) fV=120Hz, fDCLK = 297.0MHz, V_{DD} = 12.0V, DC Current.
(3) Power dissipation check pattern (LCD Module only)

a) Black Pattern   b) White Pattern   c) H-STRIPE
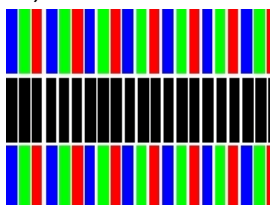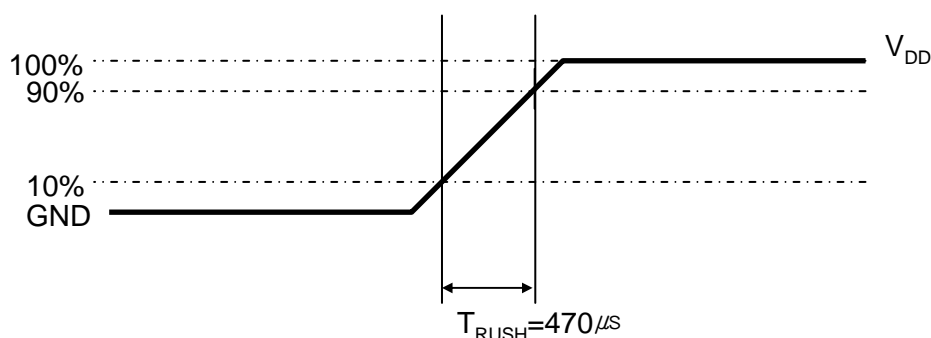
(4) Measurement Conditions
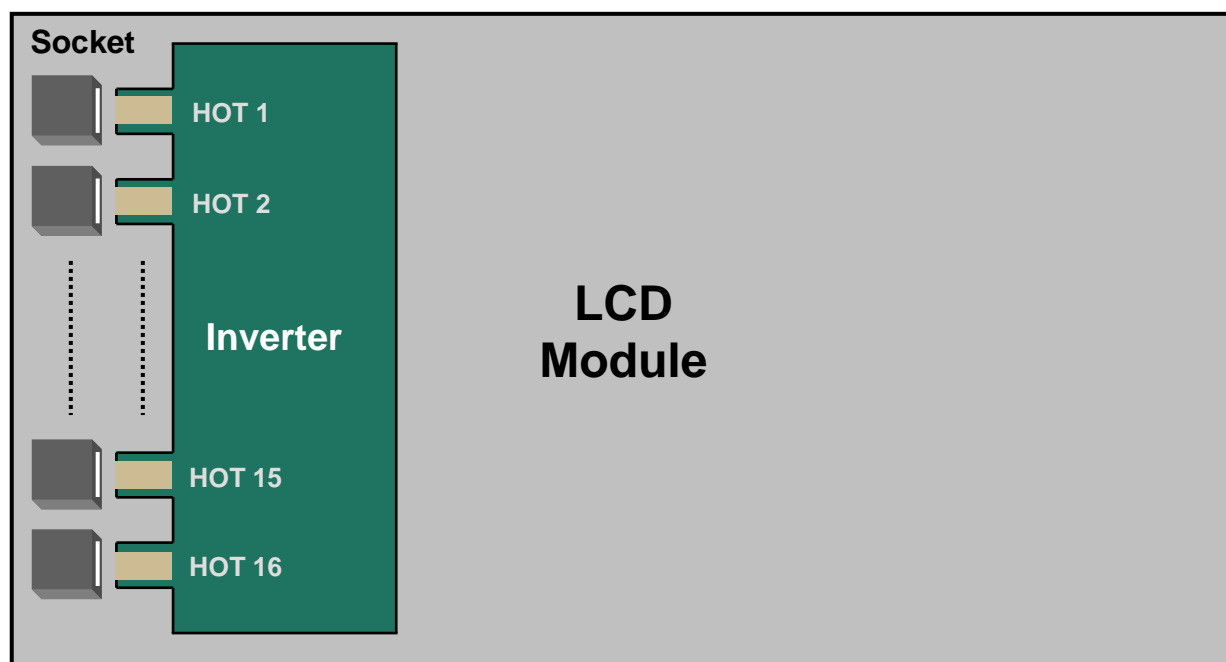
100%
90%

10%
GND

V_{DD}

T_{RUSH}=470μs

Rush Current I_{RUSH} can be measured when T_{RUSH}. is 470μs.

3.2 Back Light Unit

The back light unit contains 16 direct-lighting type CCFLs (Cold Cathode Fluorescent Lamp)

Ta=25 $\pm$ 2°C

```
┌─────────────────────────────────────────────────────────┐
│ Socket                                                   │
│                                                          │
│   ▨   ▭ HOT 1                                            │
│                                                          │
│   ▨   ▭ HOT 2               LCD                          │
│                             Module                       │
│   ┊   ┊    Inverter                                      │
│                                                          │
│   ▨   ▭ HOT 15                                           │
│                                                          │
│   ▨   ▭ HOT 16                                           │
│                                                          │
└─────────────────────────────────────────────────────────┘
```

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Life Time	Hr	50,000	-	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original

value.

[Operating condition : Ta = 25$\pm$2℃, For single lamp only.]

3.3 Inverter Input Condition & Specification

Items	Symbol	Conditions	Specifications			Unit	Note
			Min.	Typ.	Max.		
Input Voltage	Vin	-	22	24	26	V	Ta=25±2 °C (2)
Input Current	I_{RUSH}	Vin=24.0V Vdim =3.3V	-	-	10.72	A	(1)
Lamp Current	$I_{O,MAX}$	Vdim =3.3 V	10.4	11	11.6	mArms	
Shut-down Time	$T_{\_SD}$	Vin = 24V Vdim =0~3.3V	1.0	1.5	2.0	Sec	-
Backlight On/Off	ON	Vin=24.0 V	2.4	-	5.25	V	(2)
	OFF	Vin=24.0 V	0	-	0.8		
Dimming Control	V_{DIM}	Max Lum	3.3	-	-	V	(2)
		Min. Lum	-	-	0		
PWM Duty	Duty	Vin=24.0 V	20	-	100	%	(3)

Note) Power Consumption is measured when 450 [cd/m] of luminance which is the typical luminance.
Lamp Current is measured at the point before Lamp.

(1) Max Value of the Power Consumption is measured after 60 min warm-up.
(2) The ripple voltage should be controlled under 10% of Input Signal

- Additional Appendix for Supply Current & Power consumption

Items	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input Current	Iin _ overshoot	Vin = 24V, Dim=3.3V (Within 1hr at BLU on)	-	8.3	8.85	A
	Iin _ saturation	Vin = 24V, Dim=3.3V (After 1hr Aging)	-	8.06	8.59	A
Power Consumption (Back light)	P _ overshoot	Vin = 24V, Dim=3.3V (Within 1hr at BLU on)	-	199.2	212.4	Watt
	P _ saturation	Vin = 24V, Dim=3.3V (After 1hr Aging)	-	193.4	206.2	Watt

* Initial turn-on time : From 0sec to 60min after turn-on

4. Input Terminal Pin Assignment

4.1.1 Input Signal & Power   Connector : FI-RE41S-HF (JAE/UJU)

Pin	Description		Pin	Symbol	Description
1	Vdd(12V)		21		Rx1[3]P
2	Vdd(12V)		22		Rx1[4]N
3	Vdd(12V)		23		Rx1[4]P
4	Vdd(12V)		24		GND
5	Vdd(12V)		25		Rx3[0]N
6	No Connection		26		Rx3[0]P
7	GND		27		Rx3[1]N
8	GND		28		Rx3[1]P
9	GND		29	ODD LVDS SIGNAL	Rx3[2]N
10	ODD LVDS SIGNAL	Rx1[0]N	30		Rx3[2]P
11		Rx1[0]P	31		GND
12		Rx1[1]N	32		Rx3CLK-
13		Rx1[1]P	33		Rx3CLK+
14		Rx1[2]N	34		GND
15		Rx1[2]P	35		Rx3[3]N
16		GND	36		Rx3[3]P
17		Rx1CLK-	37		Rx3[4]N
18		Rx1CLK+	38		Rx3[4]P
19		GND	39		GND
20		Rx1[3]N	40		No Connection
			41		No Connection

Note) No Connection: This PINS are only used for SAMSUNG internal using.

4.1.2 Input Signal & Power

Connector : FI-RE51S-HF (JAE/UJU)

Pin	Description		Pin	Description	
1	Vdd(12V)		26		Rx4[0]P
2	Vdd(12V)		27		Rx4[1]N
3	Vdd(12V)		28		Rx4[1]P
4	Vdd(12V)		29		Rx4[2]N
5	Vdd(12V)		30		Rx4[2]P
6	No Connection		31	EVEN LVDS SIGNAL	GND
7	GND		32		Rx4CLK-
8	GND		33		Rx4CLK+
9	GND		34		GND
10	EVEN LVDS SIGNAL	Rx2[0]N	35		Rx4[3]N
11		Rx2[0]P	36		Rx4[3]P
12		Rx2[1]N	37		Rx4[4]N
13		Rx2[1]P	38		Rx4[4]P
14		Rx2[2]N	39	GND	
15		Rx2[2]P	40	No Connection	
16		GND	41	No Connection	
17		Rx2CLK-	42	No Connection	
18		Rx2CLK+	43	No Connection	
19		GND	44	No Connection	
20		Rx2[3]N	45	LVDS Option * Note(1)	
21		Rx2[3]P	46	No Connection	
22		Rx2[4]N	47	No Connection	
23		Rx2[4]P	48	No Connection	
24		GND	49	No Connection	
25		Rx4[0]N	50	No Connection	
			51	No Connection	

NOTE) No connection : This Pins are only used for SAMSUNG internal using

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Note) Pin number starts from Right side

PCB

Pin No. 1   Pin No. 41   Pin No. 1   Pin No. 51
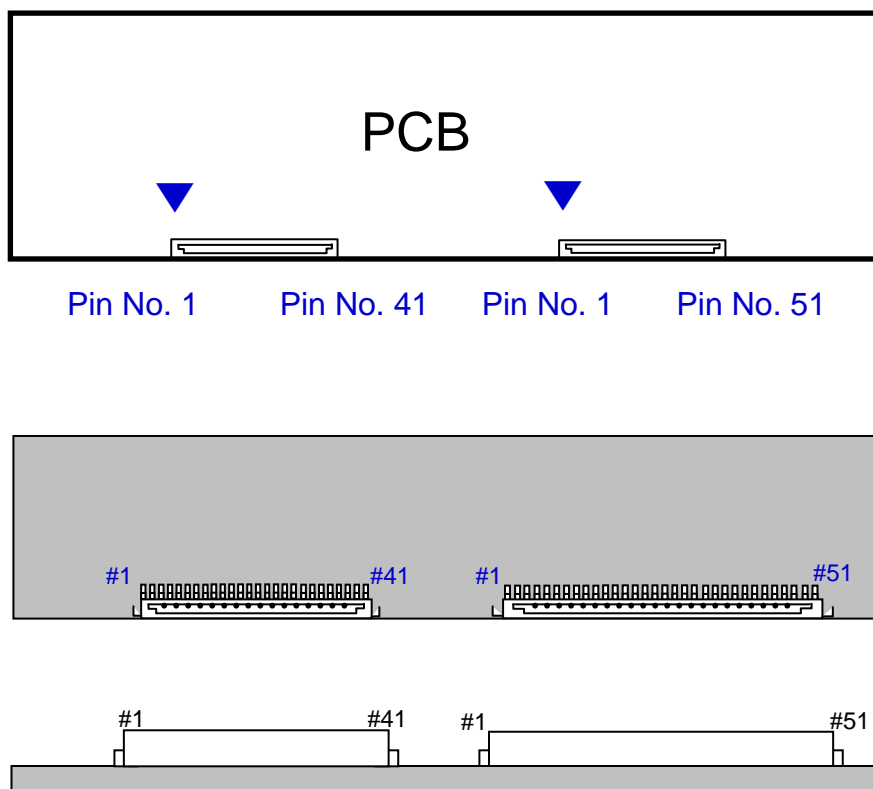
#1   #41   #1   #51

#1   #41   #1   #51

Fig. Connector diagram

a. All GND pins should be connected together and also be connected to the LCD's metal chassis.

b. All power input pins should be connected together.

c. All NC pins should be separated from other signal or power.

Note(1) LVDS OPTION : If this PIN HIGH (3.3V) → Normal LVDS format
                                            LOW (GND) → JEIDA LVDS format
         SEQUENCE   : On = VDD(T1) → LVDS Option → Interface Signal(T2)
                          OFF = Interface Signal(T3) → LVDS Option → VDD

Vin

T2   T3

LVDS
Option
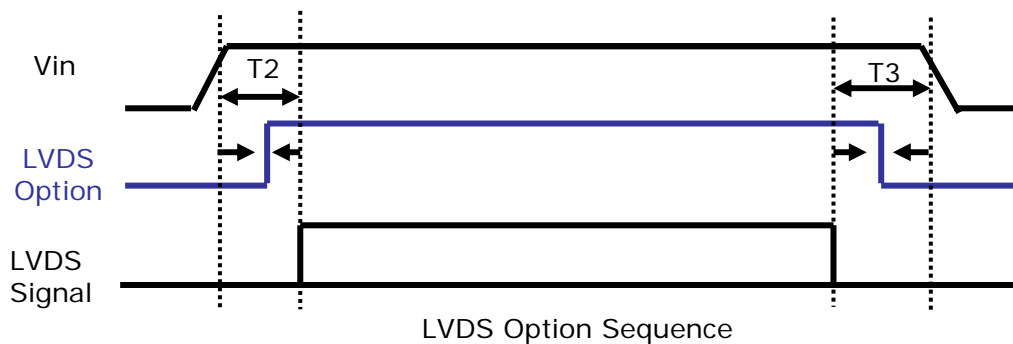
LVDS
Signal

LVDS Option Sequence

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4.2. Inverter Input Pin Configuration

Connector : 20022WR-14B1 (Yeon-ho)

Pin No.	Pin Configuration(FUNCTION)
	Master
1	24 V
2	24 V
3	24 V
4	24 V
5	24 V
6	GND
7	GND
8	GND
9	GND
10	GND
11	Error Out
12	Backlight On /Off [ON: 2.4 − 5.25 V, OFF: 0 - 0.8 V]
13	Dimming Control [0V: Min, 3.3V: Max]
14	NC

4.3. Inverter Input Power Sequence
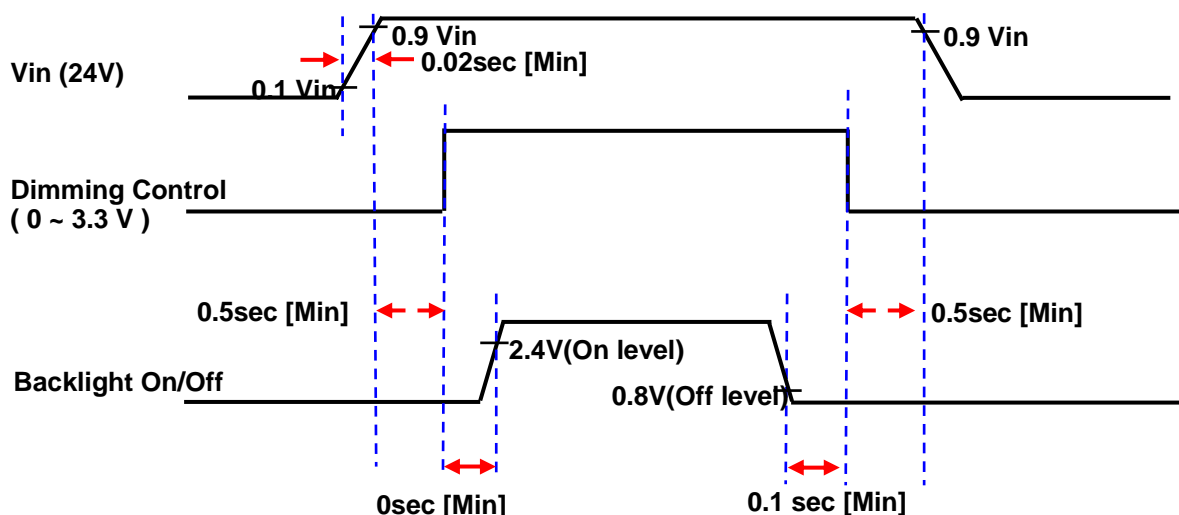
Note) SEQUENCE : ON = Vin(24V) > Dimming Control ≥ Backlight On/Off
OFF = Backlight On/Off ≥ Dimming Control > Vin(24V)

4.4 LVDS Interface
 - LVDS Receiver : Tcon (merged)
 - Data Format (JEIDA & Normal)

	LVDS pin	JEIDA -DATA	Normal -DATA
TxOUT/RxIN0	TxIN/RxOUT0	R4	R0
	TxIN/RxOUT1	R5	R1
	TxIN/RxOUT2	R6	R2
	TxIN/RxOUT3	R7	R3
	TxIN/RxOUT4	R8	R4
	TxIN/RxOUT6	R9	R5
	TxIN/RxOUT7	G4	G0
TxOUT/RxIN1	TxIN/RxOUT8	G5	G1
	TxIN/RxOUT9	G6	G2
	TxIN/RxOUT12	G7	G3
	TxIN/RxOUT13	G8	G4
	TxIN/RxOUT14	G9	G5
	TxIN/RxOUT15	B4	B0
	TxIN/RxOUT18	B5	B1
TxOUT/RxIN2	TxIN/RxOUT19	B6	B2
	TxIN/RxOUT20	B7	B3
	TxIN/RxOUT21	B8	B4
	TxIN/RxOUT22	B9	B5
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R2	R6
	TxIN/RxOUT5	R3	R7
	TxIN/RxOUT10	G2	G6
	TxIN/RxOUT11	G3	G7
	TxIN/RxOUT16	B2	B6
	TxIN/RxOUT17	B3	B7
	TxIN/RxOUT23	RESERVED	RESERVED
TxOUT/RxIN4	TxIN/RxOUT28	R0	R8
	TxIN/RxOUT29	R1	R9
	TxIN/RxOUT30	G0	G8
	TxIN/RxOUT31	G1	G9
	TxIN/RxOUT32	B0	B8
	TxIN/RxOUT33	B1	B9
	TxIN/RxOUT34	RESERVED	RESERVED

4.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY (8bit)	DATA SIGNAL RED										GREEN										BLUE										GRAY SCALE LEVEL
		R0	R1	R2	R3	R4	R5	R6	R7	R8	R9	G0	G1	G2	G3	G4	G5	G6	G7	G8	G9	B0	B1	B2	B3	B4	B5	B6	B7	B8	B9	
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~ R1020
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓ LIGHT	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1021
		0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1022
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1023
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
	DARK ↑	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~ G1020
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓ LIGHT	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1021
		0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1022
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1023
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	B1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	B2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~ B1020
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓ LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	B1021
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	B1022
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	B1023

Note) Definition of Gray :
Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)
Input Signal : 0 = Low level voltage, 1 = High level voltage

5. Interface Timing

5.1 Timing Parameters （DE mode）

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	240	297.0	310	MHz	-
Hsync		F_H	100	135.0	140	KHz	-
Vsync		F_V	90	120.0	125	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	-	1080	-	Lines	-
	Vertical Total	T_V	1090	1125	1380	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	-	1920	-	Clocks	-
	Horizontal Total	T_H	2092	2200	2350	clocks	-

Note) This product is DE mode. But the Hsync & Vsync signal must be inputted

(1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system
(2) Internal VDD = 3.3V
(3) Spread spectrum
   - Modulation rate (max) : $\pm$ 1.5 %
   - Modulation Frequency : under 100KHz

5.2 LVDS Input Data Characteristics

ITEM		SYMBOL	Min.	Typ.	Max.	UNIT	NOTE
Input Data Position	F_{IN}=78MHz	t_{RSRM}	-	-	450	ps	
		t_{RSLM}	-450	-	-	ps	
Input common mode voltage		V_{CM}	0.3	-	1.8	V	-
Differential Input Voltage		$|V_{ID}|$	200	350	600	mV	-

Note) When the skew is measured the Spread Spectrum should be 0%

5.2 Timing diagrams of interface signal (DE mode)

| MODEL | LTA550HJ07 | Doc. No | 06−001−G−20100408 | Page | 19 / 28 |
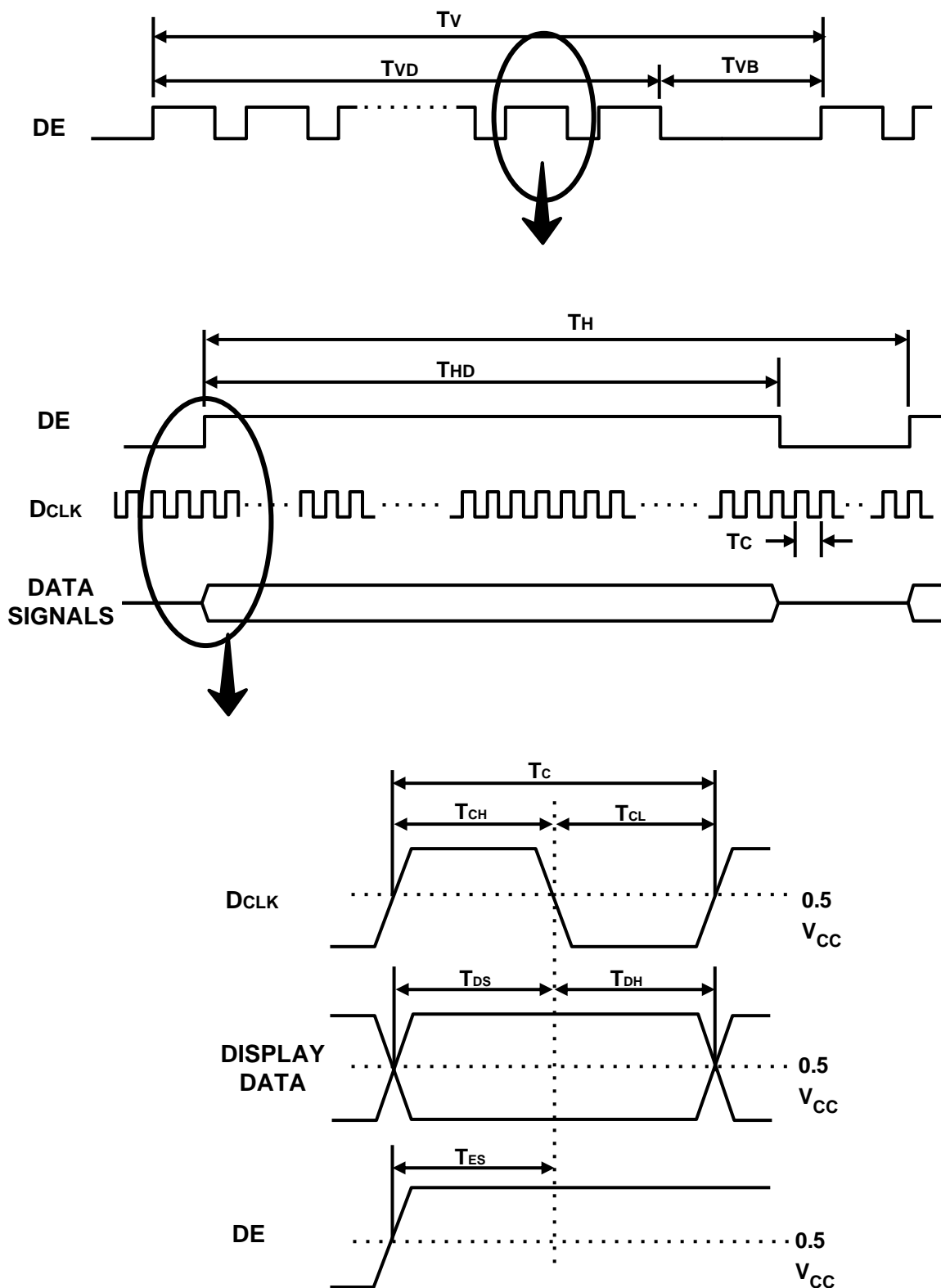
5.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.

Power Supply VDD

0msec<T1≤10msec
0<T2≤50msec
0<T3≤50msec
1000msec≤T4

Signals

1000msec≤T5
(Recommend Value)
100msec≤T6
(Recommend Value)

Power On   Power Off

0.5 Vcc
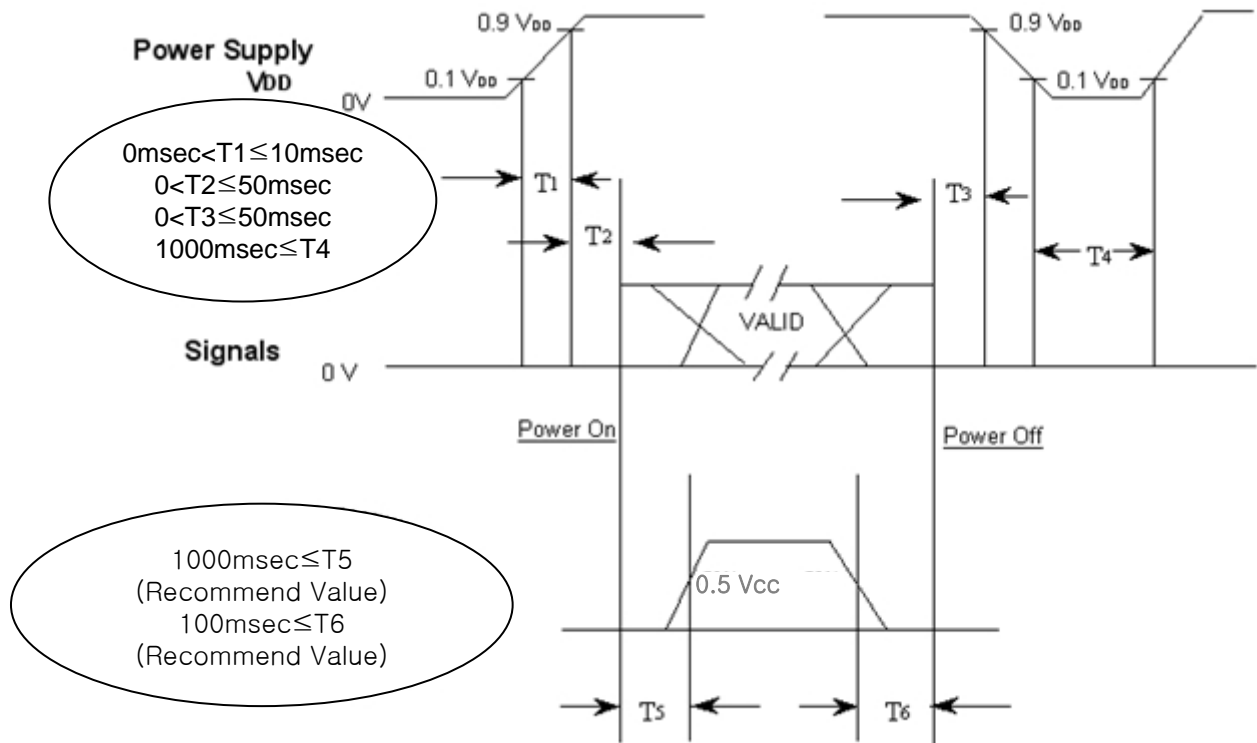
T1 : V_{DD} rising time from 10% to 90%
T2 : The time from V_{DD} to valid data at power ON.
T3 : The time from valid data off to V_{DD} off at power Off.
T4 : V_{DD} off time for Windows restart
T5 : The time from valid data to B/L enable at power ON.
T6 : The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.
- In Case T5 is less than 1000msec and T6 is less than 100msec, Garbage Display can be seen. (It is not related to electrical function issue, Just for recommendation to prevent Garbage Display)
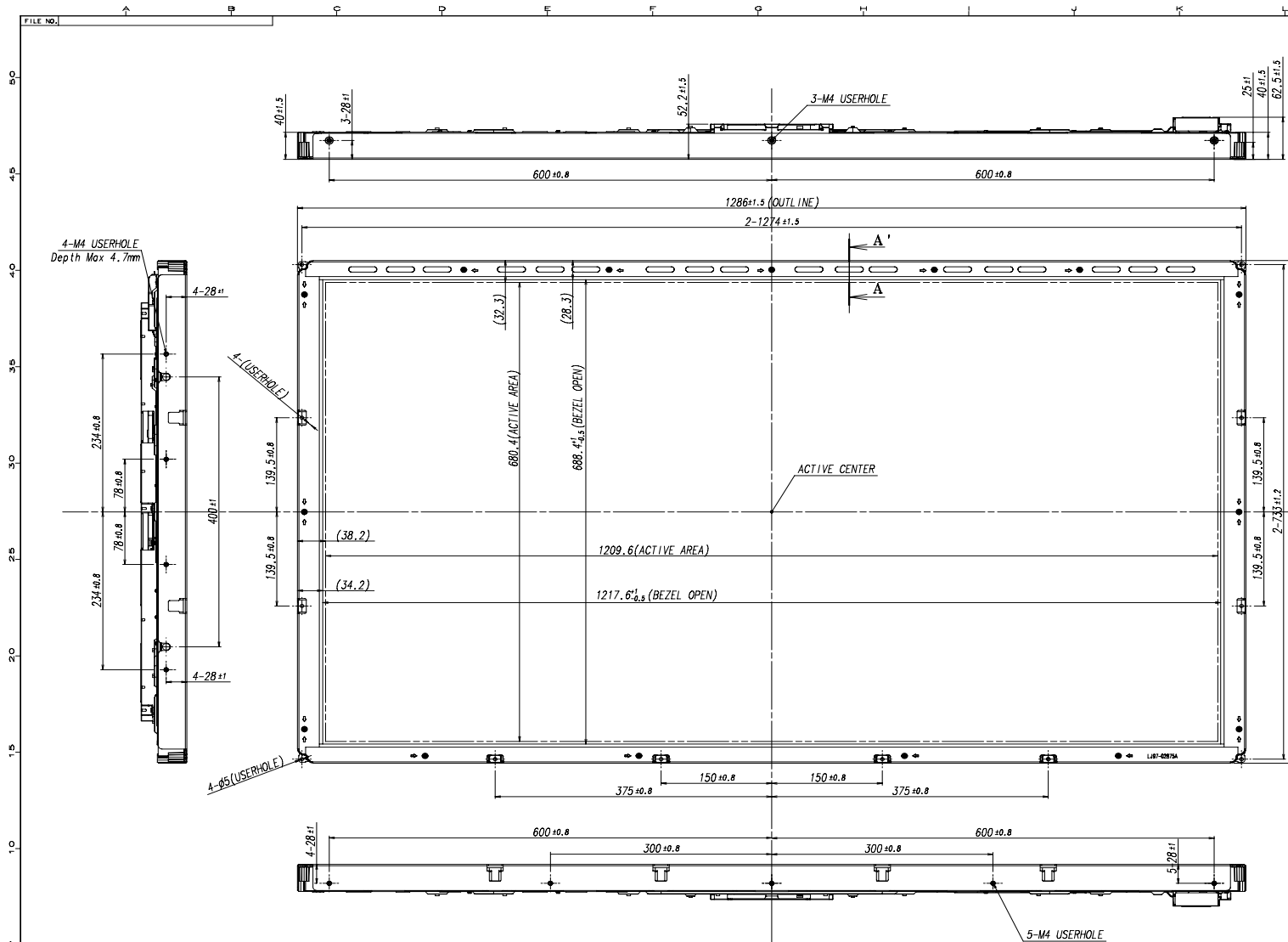
NO	PART NAME	CODE NO	SPECIFICATION	Q'TY	WEIGHT		UNFOLDED DIM.	REMARK
					FINISH	MATERIAL	OF. MATERIAL	
	OUTLINE DIMENSION(FRONT)		LTA550HJ07-C01					

3-M4 USERHOLE

52.2±1.5

25±1
40±1.5
62.5±1.5

40±1.5

3-28±1

600 ±0.8

600 ±0.8

40.0±1.5

MAX 1.5(GAP)

2.5±0.5

SECTION A-A (2:1)

1286±1.5 (OUTLINE)

2-1274±1.5

A'

A

4-M4 USERHOLE
Depth Max 4.7mm

4-28±1

4-28±1

4-(USERHOLE)

254±0.8

78±0.8

78±0.8

400±1

139.5±0.8

(32.3)

(28.3)

680.4(ACTIVE AREA)

688.4⁺¹₋₀.₅(BEZEL OPEN)

254±0.8

78±0.8

400±1

139.5±0.8

139.5±0.8

2-733±1.5 (OUTLINE)

745±1.2

ACTIVE CENTER

(38.2)

(34.2)

1209.6(ACTIVE AREA)

1217.6⁺¹₋₀.₅ (BEZEL OPEN)

139.5±0.8

78±0.8

254±0.8

2-Ø10

4-M4 USERHOLE
Depth Max 4.7mm

254±0.8

4-Ø5(USERHOLE)

LJ07-02875A

150 ±0.8

150 ±0.8

375 ±0.8

375 ±0.8

4-28±1

600 ±0.8

300 ±0.8

300 ±0.8

600 ±0.8

5-28±1

5-M4 USERHOLE

* NOTES

1.BACKLIGHT : 16 CCFL LAMPS.

2.WEIGHT SPEC. : Max 18,000g

3.Torque Spec. : Machine Screw :5.0 ~ 5.5 kgf·cm
   Taptite Screw :3.0 ~ 3.5 kgf·cm

4.USER HOLE: ALLOWED DEPTH OF USERHOLE SCREW INSERTION IS MAX 4.7mm.

5.BLACK MATRIX SPEC
   - |A - B| ≤ 2.0 mm
   - |C - D| ≤ 2.0 mm

BLACK MATRIX

6.CHASSIS TOP & POL GAP SPEC : MAX1.5mm
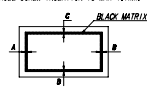
GENERAL TOLERANCE			
STEP	LEVEL 1	LEVEL 2	LEVEL 3
0 < X ≤ 4	±0.05	±0.1	±0.2
4 < X ≤ 16	±0.08	±0.15	±0.3
16 < X ≤ 64	±0.12	±0.25	±0.5
64 < X ≤ 256	±0.25	±0.4	±0.8

SAMSUNG ELECTRONICS

REV	DATE	DESCRIPTION OF REVISION	
UNIT mm	DRA'N BY	DES'D BY	CHK'D BY	APP'D BY
SCALE N/S	S.W.CHUNG		
TOLERANCE LEVEL 3	2010.01.11		
SPEC. NO			

MODEL NAME: LTA550HJ07-C01

PART/SHEET NAME: OUTLINE DIMENSION(FRONT) SHEET 1/2

CODE NO.   REV. 000

FILE NO.

4-USER MOUNT EMBOSS
(HEIGHT=4mm)

2-USER MOUNT EMBOSS
(HEIGHT=4mm)

ALIGN EMBOSS
(φ5.0, HEIGHT=2.5mm)

8-M4 USERHOLE

8-M4 USERHOLE

4-USERHOLE D7.0(BURR)

ALIGN EMBOSS
(φ5.0, HEIGHT=2.5mm)

LAMP SUPPORT HOOK
(φ5.3, HEIGHT=3.2±1.0)

ALIGN EMBOSS
(φ5.0, HEIGHT=2.5mm)

6-USER MOUNT EMBOSS
(HEIGHT=4mm)

M4 USERHOLE

4-USER MOUNT EMBOSS
(HEIGHT=4mm)

ALIGN EMBOSS
(φ5.0, HEIGHT=2.5mm)

M4 USERHOLE

4-M4 USERHOLE

ALIGN EMBOSS
(φ5.0, HEIGHT=2.5mm)

ALIGN EMBOSS
(φ5.0, HEIGHT=2.5mm)

ALIGN EMBOSS
(φ5.0, HEIGHT=2.5mm)

2-USER MOUNT EMBOSS
(HEIGHT=4mm)

BEAD HIGHT = MAX 2.0mm

2-ALIGN EMBOSS
(φ5.0, HEIGHT=2.5mm)

2-USER ALIGNMENT HOLE

4-M4 USERHOLE

Dimensions: 446, 453.6, 377, 377, 180, 194, 140, 140, 89.6, 89.6, 83.1, 83.1, 30, 24, 277.1±, 553.5±, 200, 125.8, 22.0, 303, 300, 295, 230, 215, 100, 60.5±1, 60.5±1, 73±1, 73±1, 60.5±1, 60.5±1, 150±1, 150±1, 20.4±1, 99±1, 104.4±1, 13±0.3, 10±0.3

7. Reliability Test

Item	Test condition	Quantity
Temperature Step Stress	-20 ~ 60℃, 40hr, 5 Cycle determination	4EA
HTOL	50℃, 500hr determination	4EA
LTOL	0℃, 500hr determination	4EA
HTS	70℃, 500hr determination	4EA
LTS	-30℃, 500hr determination	4EA
THB	40℃ / 95%RH, 500hr determination	4EA
WHTS	60℃ / 75%RH, 250hr determination	4EA
Thermal Shock	-20 ℃ ~ 60 ℃, 200cycle determination	4EA
ESD (operation)	contact : ±10 kV ,150pF/330Ω,200Point,1 time/Point non-contact : ±20 kV,150pF/330Ω,200Point,1 time/Point	3EA
Inverter Input Con. ESD	contact : ±2kV,150pF/330,Input Con.Pin,3 times/Pin	3EA
Vibration	10~300Hz/1.5G/10minSR, XYZ, 30min/axis	3EA
Shock	Half Sine, 11msec, ±X,Y 30G, ± Z 30G 1time/axis	3EA
PALLET Vibration	1.05Grms 5~200Hz 1hr	1PALLET(9EA)
PALLET Drop	4 edge 1face(bottom) 20 cm	1PALLET(9EA)

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these should be no change which may affect practical display functions.

```
*    HTOL/ LTOL  : High/Low  Temperature Operating Life
**   THB         : Temperature Humidity Bias
***  HTS/LTS     : High/Low Temperature Storage
**** WHTS        : Wet High Temperature Storage
```
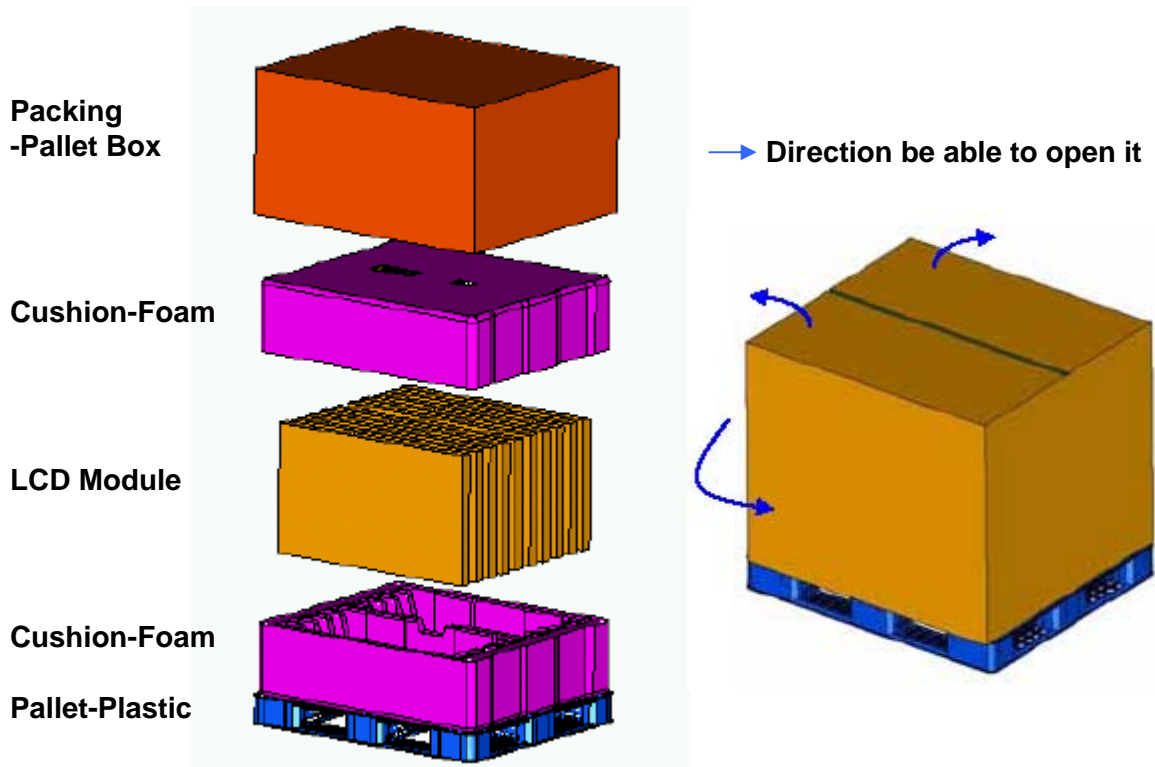
8. PACKING

8.1 CARTON (Internal Package)
(1) Packing Form
    Corrugated fiberboard box and corrugated cardboard as shock absorber
(2) Packing Method

Packing -Pallet Box

Cushion-Foam

LCD Module

Cushion-Foam

Pallet-Plastic

→ **Direction be able to open it**

8.2 Packing Specification

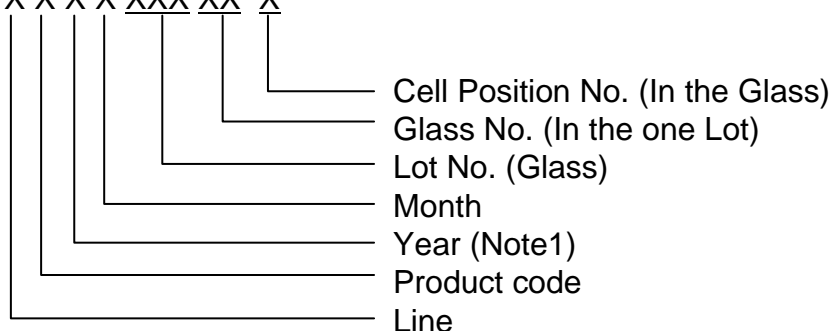| Item	Specification	Remark
LCD Packing	9 ea / (Packing-Pallet Box)	1. 162 kg / LCD (9ea) 2. 13.4 Kg / Cushion-pallet (2ea) 3. 10.5 Kg / Packing-Pallet Box (1ea) 4. Cushion-pallet Material : EPS 5. Packing-Pallet Box Material : DW4
Pallet	1Box / Pallet	1. Pallet weight = 10 kg
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1475mm(H) x 1150mm(V) x 995mm(height)
Total Pallet Weight	195.9 kg	Pallet(10kg) + Module (162 kg) + Cushion (up + bottom =13.4kg) + Pallet-BOX(10.5kg)

9. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.
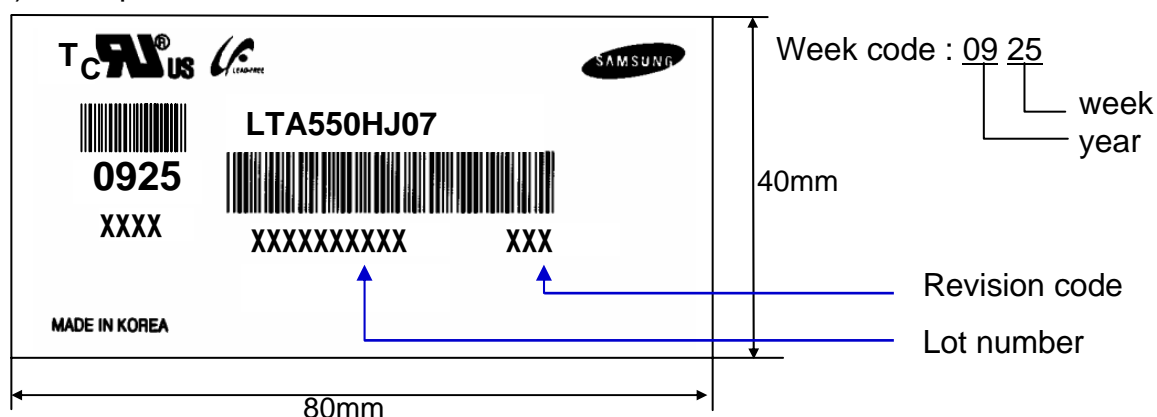
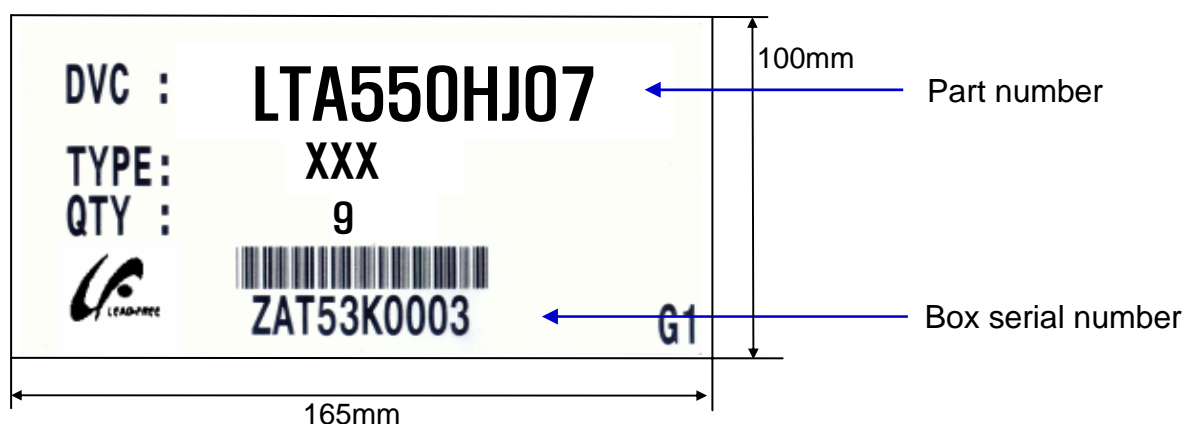(1) Part number : LTA550HJ07
(2) Revision: Three letters
(3) Lot number : X X X X XXX XX X

- Cell Position No. (In the Glass)
- Glass No. (In the one Lot)
- Lot No. (Glass)
- Month
- Year (Note1)
- Product code
- Line

(4) Nameplate Indication

Week code : 09 25
- week
- year

40mm

Revision code

Lot number

80mm

(5) Packing box attach

DVC : LTA550HJ07
TYPE: XXX
QTY : 9
ZAT53K0003   G1

100mm

Part number

Box serial number

165mm

(6) Others
1. After service part
   Lamps cannot be replaced because of the narrow bezel structure.

10. General Precautions

10.1 Handling

(a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.

(b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.

(c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFT back light.

(d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.

(e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.

(f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.

(g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.

(i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or semiconductor would be damaged.

(j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(k) Do not disassemble the Module.

(l) Do not disassemble shield case of inverter & LVDS board

(m) Do not connect N.C pins. (Samsung internal use only)

(n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handling a module

(o) Pins of I/F connector should not be touched directly with bare hands.

10.2 Storage

(a) Do not leave the Module in high temperature, and high humidity for a long time.
It is highly recommended to store the Module with temperature from 0 to 35℃
and relative humidity of less than 70%.

(b) Do not store the TFT-LCD Module in direct sunlight.

(c) The Module should be stored in a dark place. It is prohibited to apply sunlight or
fluorescent light in storing.

10.3 Operation

(a) Do not connect or disconnect the Module in the "Power On" condition.

(b) Power supply should always be turned on/off by the "Power on/off sequence"

(c) Module has high frequency circuits. Sufficient suppression to the electromagnetic
interference should be done by system manufacturers. Grounding and shielding methods
may be important to minimize the interference.

(d) The cable between the back light connector and its inverter power supply should
be connected directly with a minimized length. A longer cable between
the back light and the inverter may cause lower luminance of lamp(CCFT) and
may require higher startup voltage(Vs).

10.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions.
Normal condition is defined as below;
- Temperature : 20±15℃
- Humidity : 55±20%
- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature,
humidity, display patterns or operation time etc.., It is strongly recommended
to contact SEC for Application engineering advice. Otherwise, its reliability and
function may not be guaranteed. Extreme conditions are commonly found at
Airports, Transit Stations, Banks, Stock market, and Controlling systems.

10.5 Others

(a) Ultra-violet ray filter is necessary for outdoor operation.

(b) Avoid condensation of water. It may result in improper operation or disconnection
   of electrode.

(c) Do not exceed the absolute maximum rating value. (supply voltage variation,
   input voltage variation, variation in part contents and environmental temperature,
   and so on)
   Otherwise the Module may be damaged.

(d) If the Module keeps displaying the same pattern for a long period of time,
   the image may be "sticked" to the screen.
   To avoid image sticking, it is recommended to use a screen saver.

(e) This Module has its circuitry PCB's on the rear side and should be handled
   carefully in order not to be stressed.

(f) Please contact SEC in advance when you display the same pattern for a long time.